

## Solid State Broadband High Power Amplifier

### 1212

## 2000 - 6000 MHz / 50 Watts

The SKU 1212 is a 2000 to 6000 MHz amplifier which is guaranteed to deliver 50W minimum output power and related RF performance under all specified temperature and environmental conditions. Typical power output is 65W and other typical performance parameters are also listed as a guide for consideration, but not guaranteed. This amplifier is suitable for broadband mobile jamming and band specific high power applications in the S and C frequency bands. This compact module utilizes the latest high power RF GaN transistors and also features built-in control and monitoring, with protection functions to ensure high availability.



- Solid-state Class AB design
- Instantaneous ultra broadband
- Suitable for CW, AM, and FM (Consult factory for other modulation types)
- Small, lightweight, high reliability and ruggedness
- 50 ohm input/output impedance
- Built-in control, monitoring and protection circuits
- RS485 serial interface for monitoring and control

ELECTRICAL SPECIFICATIONS @ 28.0V<sub>DC</sub>, Over Temperature and Environmental Conditions, as specified.

Symbol	Unit	Test Condition	Min	Тур	Max
BW	MHz		2000		6000
Psat	W	CW input signal		65	
D	dRm	Variable Attenuator set to nominal attenuation. CW signal		_0	-6
I IN	ubili			-9	-0
Gss	dB		57	61	64
		Variable attenuator set to nominal attenuation.			
ΛGee	dB	Test conditions the same as Gss			±3
$G_{ADJ}$	dB	Test conditions the same as G <sub>SS</sub>	15		
GSTEP	dB	Test conditions the same as Gss	0.5		
D	dDm	CW input signal for unlimited duration			10
PIN, Max	abm	Cw input signal for unlimited duration.			10
		Measured with VNA in swept frequency mode at -20dBm and			
IRL	dB	0dBm CW. Input power calibrated / measured at the amplifier			-10
NF	dB				20
2nd	dBc				-10
_	ч				.0
3 <sup>rd</sup>	dBc	<u> </u>			-20
		l l			
C	4D.				60
Spur	abc				-60
Vnc	V		24	28	32
	-		27		
IDC	Α			7	9
T <sub>ON/OFF</sub>	uSec				1
	_				
	PSAT PIN  GSS  AGSS GADJ GSTEP PIN, Max IRL NF 2nd 3rd  Spur  VDC IDC	Psat W Pin dBm  Gss dB  ΔGss dB  GADJ dB  GSTEP dB  Pin, Max dBm  IRL dB  NF dB  2nd dBc  3rd dBc  Spur dBc  VDC V IDC A	PSAT         W         CW input signal           PIN         dBm         Variable Attenuator set to nominal attenuation. CW signal source at an output power of 50 watts.           Gss         dB         Measured with VNA in swept frequency mode at -20dBm CW. Input power calibrated / measured at the amplifier input port. Variable attenuator set to nominal attenuation.           ΔGss         dB         Test conditions the same as Gss           GADJ         dB         Test conditions the same as Gss           GSTEP         dB         Test conditions the same as Gss           PIN, Max         dBm         CW input signal for unlimited duration.           IRL         dB         Measured with VNA in swept frequency mode at -20dBm and 0dBm CW. Input power calibrated / measured at the amplifier input port. Variable attenuator set to nominal attenuation.           NF         dB         Variable attenuator set to nominal attenuation.           Variable attenuator set to nominal attenuation. CW signal source at an output power of 50W.           3rd         dBc         Variable attenuator set to nominal attenuation. CW signal source at an output power of 50W.           Spurious defined as any non-harmonic amplifier output. Spurious defined as any non-harmonic amplifier output. Spurious measured in a 1kHz resolution bandwidth, 10kHz video bandwidth. Specifications apply at offsets of greater than or equal to +/- 10kHz from the RF carrier. Maximum measurement frequency is 6.5GHz.           V <sub>DC</sub> V	PSAT         W         CW input signal         50           PIN         dBm         Variable Attenuator set to nominal attenuation. CW signal source at an output power of 50 watts.         50           GSS         dB         Measured with VNA in swept frequency mode at -20dBm CW. Input power calibrated / measured at the amplifier input port. Variable attenuator set to nominal attenuation.         57           ΔGSS         dB         Test conditions the same as Gss         15           GADJ         dB         Test conditions the same as Gss         0.5           GATEP         dB         Test conditions the same as Gss         0.5           JNA         dBm         CW input signal for unlimited duration.         0.5           Measured with VNA in swept frequency mode at -20dBm and 0dBm CW. Input power calibrated / measured at the amplifier input port. Variable attenuator set to nominal attenuation.         0.5           NF         dB         Variable attenuator set to nominal attenuation.         0.0           3rd         dBc         Variable attenuator set to nominal attenuation. CW signal source at an output power of 50W.         0.0           3rd         dBc         Variable attenuator set to nominal attenuation. CW signal source at an output power of 50W.         0.0           Spur         dBc         Spurious measured in a 1kHz resolution bandwidth, 10kHz video bandwidth. Specifications apply at offsets of greater	PsAT         W         CW input signal         50         65           PIN         dBm         Variable Attenuator set to nominal attenuation. CW signal source at an output power of 50 watts.         -9           Gss         dB         Measured with VNA in swept frequency mode at -20dBm CW. Input power calibrated / measured at the amplifier input port. Variable attenuator set to nominal attenuation.         57         61           ΔGss         dB         Test conditions the same as Gss         15           Gadu         dB         Test conditions the same as Gss         0.5           Flin, Max         dBm         CW input signal for unlimited duration.         0.5           IRL         dB         Test conditions the same as Gss         0.5           PIN, Max         dBm         CW input signal for unlimited duration.         0.5           IRL         dB         Measured with VNA in swept frequency mode at -20dBm and OdBm CW. Input power calibrated / measured at the amplifier input port. Variable attenuator set to nominal attenuation.         0.5           NF         dB         Variable attenuator set to nominal attenuation.         0.5           2nd         dBc         Variable attenuator set to nominal attenuation. CW signal source at an output power of 50W.         0.0           3rd         dBc         Variable attenuator set to nominal attenuation. CW signal source at an output



## Solid State Broadband High Power Amplifier

1212

2000 - 6000 MHz / 50 Watts

#### PA PROTECTION / RUGGEDNESS

The PA includes protection circuits for:

- Over temperature
- Over voltage
- Reverse polarity
- Over current

In addition to protection circuits, the PA will withstand full reflection at the RF output port at any angle for up to 1 minute at rated Pout

#### **ENVIRONMENTAL SPECIFICATIONS**

Parameter	Symbol	Min	Тур	Max	Unit
Operating Case Temperature	Tc	-40		+85	°C
Storage Temperature	Tstg	-40		+85	°C
Relative Humidity (non-condensing)	RH			95	%
Altitude (MIL-STD-810F Method 500.4)	ALT			30,000	Feet
Vibration/Shock MIL-STD-810F Method 514.5/516.5 – Proc 1	VI/SH		Airborne		

#### **MECHANICAL SPECIFICATIONS**

Parameter	Value	Unit
Dimension	7.0 x 4.0 x 1.1	Inch
Weight	1.5	Pound
RF Connectors Input / Output	Type-SMA, Female	J1, J2
DC Interface Connector	Hybrid – D-Sub 17-Pin, Male (17W2)	J3
Cooling	External Heatsink Required (not supplied)	



# Solid State Broadband High Power Amplifier

1212

2000 - 6000 MHz / 50 Watts

#### **DC INTERFACE CONNECTOR**

Pin#	Description	Specification
A1	GND	Ground Return
A2	VDD	Supply Voltage: +24.0 – 32.0V <sub>DC</sub> , 28.0V <sub>DC</sub> Nominal
1	RS485 (-)	Serial Communication Bus
2	Temperature Reporting	Analog Output Voltage @ 10mV/°C with a 500mV offset (i.e. 0.75V = 25°C)
3	Address 1	Hardware Address 1
4	Address 3	Hardware Address 3
5	Attenuator Setting	Voltage input in the range of $0.5-3.0V_{DC}$ , $0.5V_{DC}$ corresponds with minimum attenuation, $3.0V_{DC}$ is maximum attenuation. Leave pin open or grounded to utilize RS-485 interface. (See RS-485 details below)
6	PA Enable	0/3.3V logic levels: Power Amplifier disable is a TTL Logic Low (0V). (Internally Pulled-High 3.3V) Leave pin open or pulled high to utilize RS-485 interface.(See RS-485 details below)
7	Alarm	Amplifier Alarm indicator: Normally TTL Low A logic High indicates a fault condition, 0/3.3V Logic Levels
8	RS485 (+)	Serial Communication Bus
9	Current Reporting	Analog output voltage range of 1V/10A (i.e. 1.5V = 15A)
10	Address 0	Hardware Address 0 – Least significant bit
11	Address 2	Hardware Address 2
12	Address 4	Hardware Address 4 – Most significant bit
13	Not Used	No Connection
14	Not Used	No Connection
15	Reset	Hardware reset Logic 0 to reset PA and clear latched faults

### **OUTLINE DRAWING**

