

Solid State Broadband High Power Amplifier

1012 – BBM4A5ACJ

1000 – 2000 MHz / 15 Watts

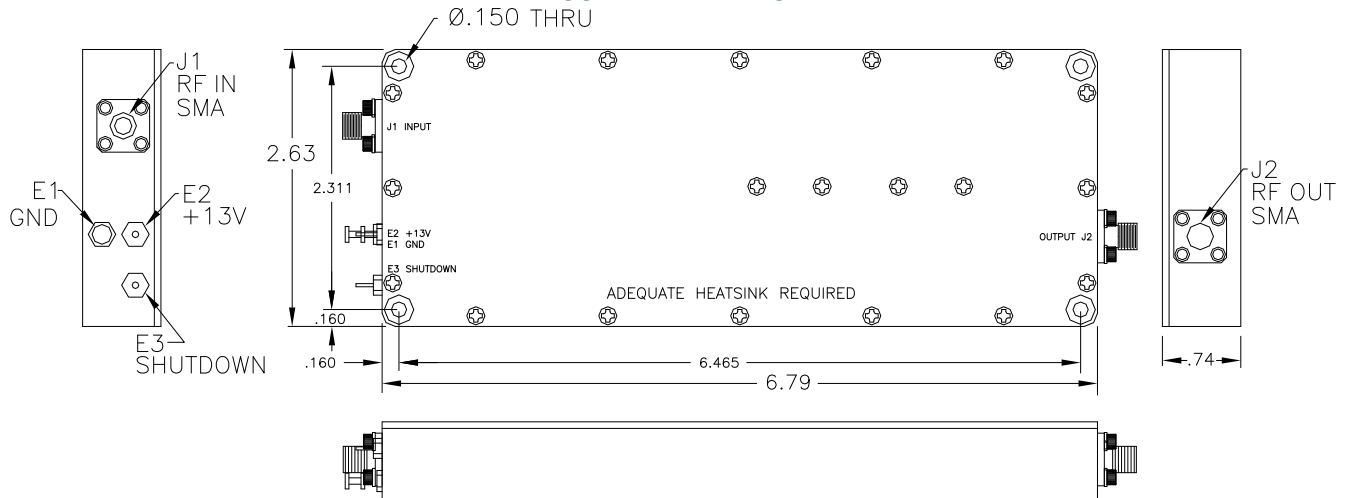
LIMITS

Input RF drive level without damage	+10 dBm	Max
Load VSWR @ P _{OUT} = 12W	∞ @ all load phase & amplitude for duration of 1 minute 3:1 @ all load phase & amplitude continuous	-
Thermal Overload	85°C shutdown	Max

DC INTERFACE CONNECTORS – Feed Thru/Terminal Post

Pin #	Description	Specification
E1	GND	Ground
E2	+VDD	+12.0-15.0V _{DC}
E3	Shutdown	Amplifier Disable: TTL Logic High (5V) (Internally Pulled-Low)

OUTLINE DRAWING



TYPICAL PERFORMANCE PLOTS

Plot 1 – Small Signal Gain and P_{1dB}

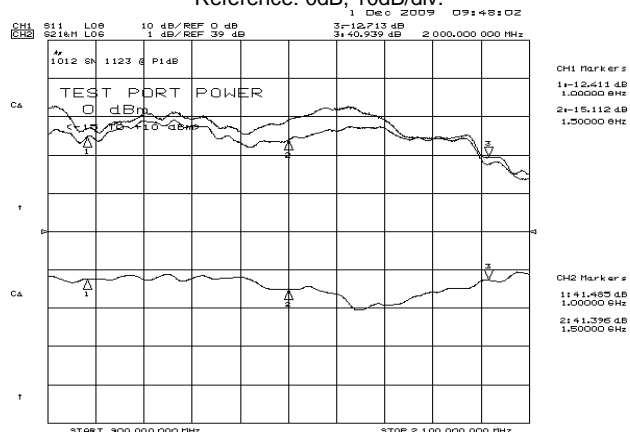
Top Curve: Small Signal Gain @ P_{IN} = -20dBm

Middle Curve: Power Gain @ P_{1dB}, P_{IN} = 0Bm

Reference: 39dB, 1dB/div.

Bottom Curve: Input Return Loss

Reference: 0dB, 10dB/div.



Plot 2 – Small Signal Gain and P_{SAT}

Top Curve: Small Signal Gain @ P_{IN} = -20dBm

Middle Curve: P_{SAT} @ P_{IN} = 2dBm

Reference: 39dB, 1dB/div.

Bottom Curve: Input Return Loss

Reference: 0dB, 10dB/div.

