

Solid State Personal Communication Power Amplifier

7005 - PCM3J3KEM
465 – 515 MHz / 25 Watts for TETRA & SMR

The PCM3J3KEM (SKU 7005) is suitable for Ultra linear SMR & TETRA repeater and MicroCell applications. Also suitable for other digital modulation applications, this amplifier utilizes proprietary DIP™ (Direct Injection Pre-D) circuit and linear LDMOS power devices that provide ample output power margins, high gain, wide dynamic range, and excellent group delay and phase linearity. Exceptional performance, long term reliability, and high efficiency are achieved by employing advanced matching networks and combining techniques, EMI/RFI filters, machined housings, and qualified components. This rugged module is input overdrive and output isolator protected, and proprietary ALC circuits ensure stable, ripple free output power under multi-channel conditions. Empower RF's ISO9001 Quality Assurance Program assures consistent performance and the highest reliability.



- Solid-state linear design
- Small and lightweight
- Suitable for CW, SMR, TETRA
- 50 Ohm Input/Output impedance
- High reliability and ruggedness
- Built-in monitoring circuit and Output Isolator

ELECTRICAL SPECIFICATIONS @ +28V_{DC}, 25°C, 50Ω System

Parameter	Symbol	Min	Typ	Max	Unit
Operating Frequency	BW	465		515	MHz
Output Power CW	P _{SAT}	40			Watt
Output Power @ 1dB Gain Compression	P _{1dB}	25			Watt
Small Signal Gain	G _{SS}	46	-	48	dB
Small Signal Gain Flatness	ΔG _{SS}		±0.5	±0.75	dB
Third Order Intercept Point 2-Tone @ 33dBm/Tone, Spacing 25-500kHz	IP3	+55			dBm
Input/Output Return Loss	S ₁₁ /S ₂₂			-14	dB
Noise Figure	NF		7	10	dB
Harmonics @ P _{OUT} = 25W	H			-45	dBc
Spurious Signals	Spur		-70	-60	dBc
Operating Voltage	V _{DC}	27	28	29	Volt
Current Consumption @ P _{OUT} = 25W CW	I _{DD}		3.0		Amp
Current Consumption @ P _{OUT} = 4W Composite	I _{DD}		1.6	2.0	Amp

MECHANICAL SPECIFICATIONS

Parameter	Value	Units	Limits
Dimensions	5.0 x 3.75 x 1.0	Inch	Max
Weight	1.0	lb.	Max
RF Connectors Input / Output	Type-SMA, Female		
DC Interface Connector	D-sub, 9-pin, Male		
Cooling	External Heatsink (not supplied)		

ENVIRONMENTAL CHARACTERISTICS (Design to Meet)

Parameter	Symbol	Min	Typ	Max	Unit
Operating Case Temperature	T _C	-10		+50	°C
Non-operating Temperature	T _{STG}	-40		+85	°C
Relative Humidity (non-condensing)	RH			95	%
Altitude (MIL-STD-810F Method 500.4)	ALT			30,000	Feet
Vibration / Shock MIL-STD-810F - Method 514.5/516.5 – Proc I	VI / SH		Airborne		

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LIMITS

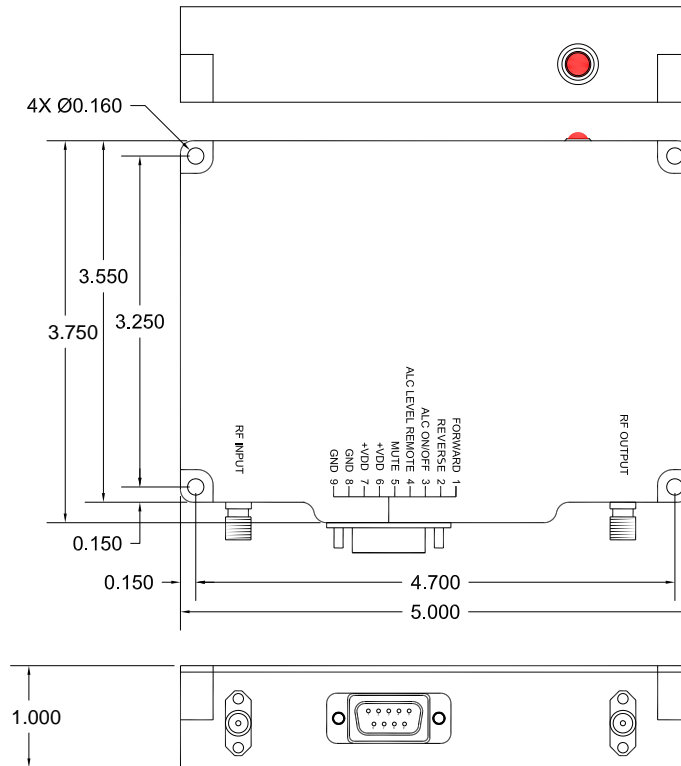
Input RF drive level without damage	+6 dBm	Max
Load VSWR @ P _{OUT} = 25W	∞ @ all load phase & amplitude continuous (Built-in Isolator)	-
Thermal Overload	85°C shutdown	Max

DC INTERFACE CONNECTOR – D-sub 9-pin, Male

Pin #	Description	Specifications
1	Forward Power Monitor	Continuous Analog voltage relative to forward power level FWDM: 24-44 dBm @ 0-5V (200mV/dB), 44dBm = 4.4V _{DC}
2	Reverse Power Monitor	Continuous Analog voltage relative to reflected power level REVM: 20-40dBm @ 0-5V (150mV/dB)
3	ALC ON/OFF	ALC OFF = TTL Logic High (5V) (Internally Pulled-low)
4	ALC Level	Continuous adjustable range via analog input levels Setting Point (ASP): 33-44dBm @ 0-5V (250mV/dB) Error Range (AER): ±1.5dB, Response Time (ART): 100ms/dB
5	Mute	Amplifier Disable: TTL Logic High (5V) (Internally Pulled-Low)
6&7	+VDD	+27.0-29.0V _{DC}
8&9	GND	Ground

LED	LED Indicator	Output Power level indicator referenced to ALC setting (Independent of ALC ON or OFF)
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OUTLINE DRAWING



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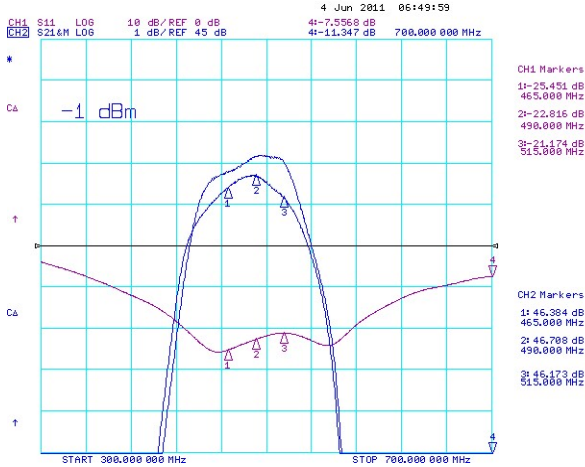
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TYPICAL PERFORMANCE PLOTS

Plot 1 – Small Signal Gain and P_{1dB}

Top Curve: Small Signal Gain @ $P_{IN} = -20dBm$
 Middle Curve: Power Gain @ P_{1dB} , $P_{IN} = -1dBm$
 Reference: 45dB, 1dB/div.
 Bottom Curve: Input Return Loss
 Reference: 0dB, 10dB/div.



Plot 2 – Small Signal Gain and P_{SAT}

Top Curve: Small Signal Gain @ $P_{IN} = -20dBm$
 Middle Curve: Power Gain @ P_{SAT} , $P_{IN} = +2dBm$
 Reference: 45dB, 1dB/div.
 Bottom Curve: Input Return Loss
 Reference: 0dB, 10dB/div.

