

## Solid State Broadband High Power Amplifier

2013 - BBS0D3FHM

0.15 – 230 MHz / 50 Watts

The BBS0D3FHM (2013) is suitable for broadband high power UHF linear applications; this amplifier utilizes push-pull MOSFET power devices that provide wide frequency response and dynamic range, high gain, low distortions, and excellent linearity. Employing advanced broadband RF matching networks and combining techniques, EMI/RFI filters, and all qualified components achieve exceptional performance, and high efficiency. The system includes a universal voltage, single phase, power supply and a built in forced air-cooling system. Empower RF's ISO9001 Quality Assurance Program assures consistent performance and the highest reliability.



SKU#: 2013CLRAAXXX

- Solid-state linear design
- Instantaneous broadband
- Small and lightweight
- Standard front panel manual gain adjust
- Suitable for CW, AM, and FM (Consult factory for other modulation types)
- 50 ohm input/output impedance
- High reliability and ruggedness

### ELECTRICAL SPECIFICATIONS @ 120V<sub>AC</sub>, 25°C, 50 Ω system

Parameter	Symbol	Min	Typ	Max	Unit
Operating Frequency	BW	0.15		230	MHz
Output Power CW	P <sub>SAT</sub>	50	75		Watt
Output Power @ 1dB Gain Compression	P <sub>1dB</sub>	30	50		Watt
Power Gain @ 1dB Gain Compression	G <sub>1dB</sub>	46			dB
Input Power for Rated P <sub>SAT</sub>	P <sub>IN</sub>		0	3	dBm
Small Signal Gain Flatness	ΔG			±1.5	dB
Gain Adjustment Range	FGA	20	25		dB
Input Return Loss	S <sub>11</sub>			-10	dB
Noise Figure	NF		10		dB
Third Order Intercept Point 2-Tone @ 37dBm/Tone, 100kHz Spacing	IP3		+53		dBm
Harmonics @ P <sub>OUT</sub> = 30W	H		-20		dBc
Spurious Signals	Spur		-70	-60	dBc
Operating Voltage (1-phase)	V <sub>AC</sub>	100		240	Volt
Power Consumption @ P <sub>OUT</sub> = 50W CW	P <sub>D</sub>			300	Watt

### MECHANICAL SPECIFICATIONS

Parameter	Value	Unit
Dimensions	19 x 3.5 x 18	Inch
Weight	27	Pound
RF Connectors Input/Output	Type-N female	
Cooling	Built-in internal forced air cooling system	

### ENVIRONMENTAL CHARACTERISTICS (Design to Meet)

Parameter	Symbol	Min	Typ	Max	Unit
Operating Temperature	T <sub>C</sub>	0		+50	°C
Non-operating Temperature	T <sub>STG</sub>	-40		+85	°C
Relative humidity w/o condensation	RH			95	%
Altitude (MIL-STD-810F Method 500.4)	ALT			30,000	Feet
Vibration / Shock MIL-STD-810F - Method 514.5/516.5 – Proc I	VI / SH		Airborne		

### LIMITS

Input RF drive level without damage	+10 dBm	Max
Load VSWR @ P <sub>OUT</sub> = 30W	∞ @ all load phase & amplitude for duration of 1 minute 3:1 @ all load phase & amplitude continuous	-
Thermal Overload	85°C shutdown	Max

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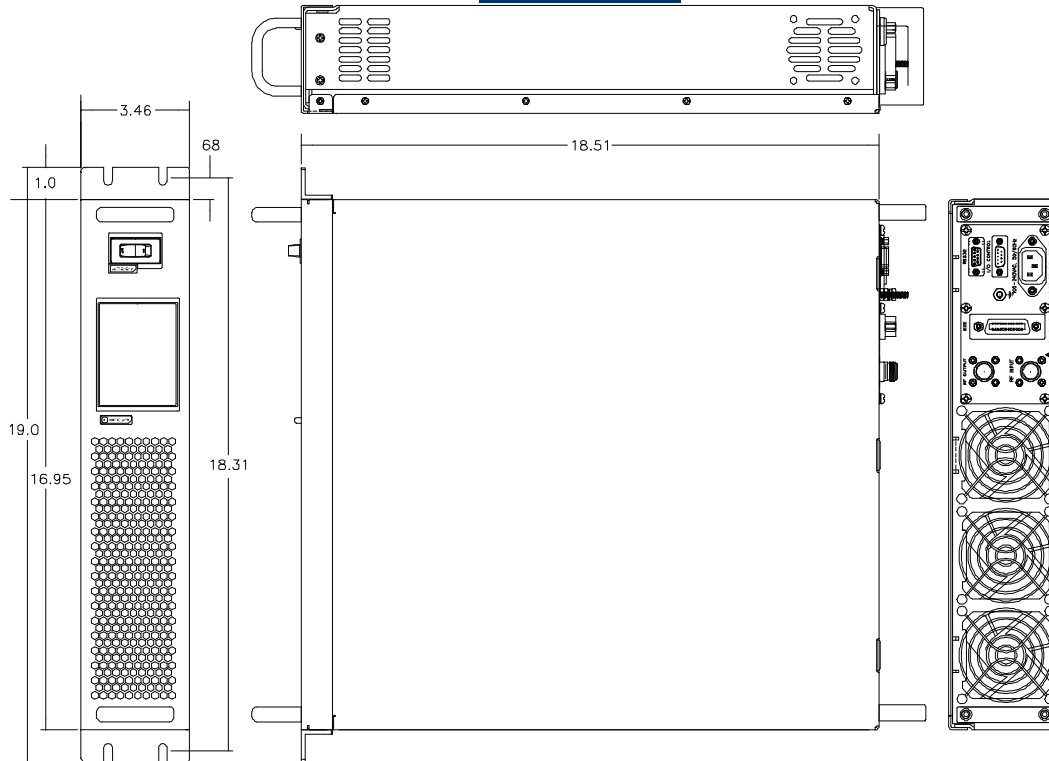
## AVAILABLE OPTIONS

SKU #	Description	LCD Touchscreen
2013CLRAAXXX	LCD controller, Rear RF connectors 100-240VAC, 50/60Hz.	Touchscreen Digital Display, including FWD/REV Power indication (dBm or Watt scale), Gain Adjustment, ALC Fast/Slow, On/Off, Standby mode, Fault indication, Rear panel GPIB/HPIB IEEE-488.2 and Half Duplex RS232. <i>Note: (Output power is lowered by 0.5-0.75dB with this option)</i>
2009CFFAAXXX	FGA (Front Gain Adjust), Rear RF connectors, 100-240VAC, 50/60Hz	
Optional	Rack Slides (Call for price)	

## I/O INTERFACE CONNECTOR – D-sub 9-pin, Female

Pin #	Description	Specifications	Options	
			FGA	LCD
1	Forward Test Point	Analog Voltage 0-5V <sub>DC</sub> relative to Forward Power Level		√
2	Reverse Test Point	Analog Voltage 0-5V <sub>DC</sub> relative to Reverse Power Level		√
3	5V Test Point	Output +5.0V <sub>DC</sub> ±0.2V	√	√
4	VVA Test Point	VVA Gain Control +5.6V <sub>DC</sub> ±0.2V	√	
5	EXT Shutdown	Amplifier Disable: TTL Logic High (5V) (Internally Pulled-Low)	√	√
6	12V Test Point	Output +12.0V <sub>DC</sub> ± 0.5V	√	√
7	P/S Test Point	Power Supply Output voltage: +26.0-30.0V <sub>DC</sub>	√	√
8	GND	Ground	√	√
9	GND	Ground	√	√

SYSTEM OUTLINE Shown  
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## TYPICAL PERFORMANCE PLOTS

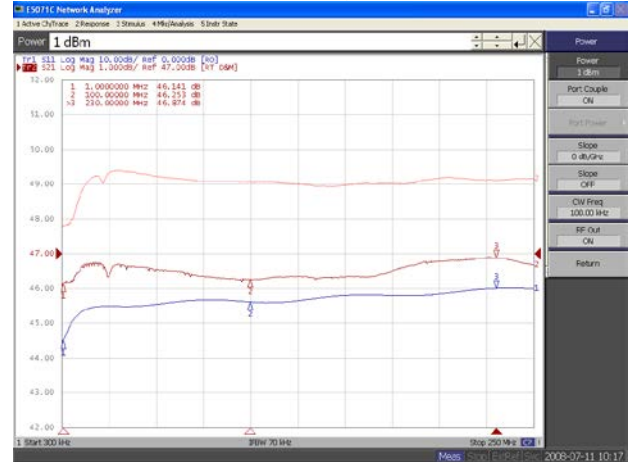
### Plot 1 – Small Signal Gain and $P_{1dB}$

Top Curve: Small Signal Gain @  $P_{IN} = -20dBm$   
 Middle Curve: Power Gain @  $P_{1dB}$ ,  $P_{IN} = -2.0dBm$   
 Reference: 47 dB, 1dB/div.  
 Bottom Curve: Input Return Loss  
 Reference: 0dB, 10dB/div.



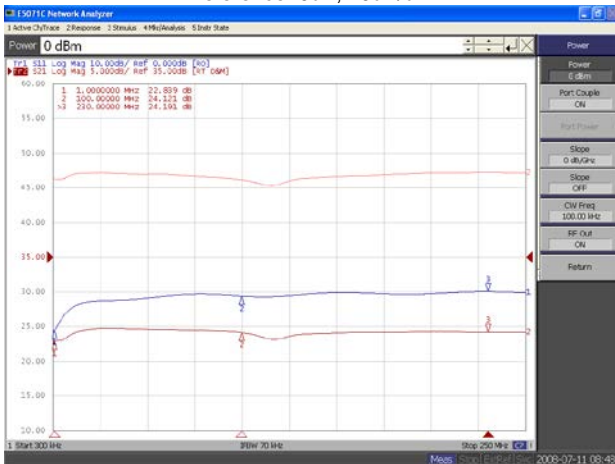
### Plot 2 – Small Signal Gain and $P_{SAT}$

Top Curve: Small Signal Gain @  $P_{IN} = -20dBm$   
 Middle Curve: Power Gain @  $P_{SAT}$ ,  $P_{IN} = 1.0dBm$   
 Reference: 47dB, 1dB/div.  
 Bottom Curve: Input Return Loss  
 Reference: 0dB, 10dB/div.



### Plot 3 – Gain Adjustment Range

Top Curve: Maximum Gain @  $P_{IN} = -20dBm$   
 Bottom Curve: Minimum Gain @  $P_{IN} = -20dBm$   
 Reference: 30dB, 5dB/div.  
 Middle Curve: Input Return Loss @ Minimum Gain  
 Reference: 0dB, 10dB/div.



### Plot 4 – ALC Flatness @ 25W & 5W

Top Curve: ALC @ 25W,  $P_{IN} = 0dBm$   
 Bottom Curve: ALC @ 5W,  $P_{IN} = 0dBm$   
 Reference: 41dB, 1dB/div.  
 Middle Curve: Input Return Loss  
 Reference: 0dB, 10dB/div.

